

	Application No.	Applicant(s)	_
Notice of Allowability	10/629,154	LEE ET AL.	
	Examiner	Art Unit	_
	Phallaka Kik	2825	
	Pilaliaka Kik	2023	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not included will be mailed in due course. THIS)
1. \boxtimes This communication is responsive to <u>Restriction/Election fi</u>	led on 7/26/2006, application and ID	S filed on 7/29/2003.	
2. The allowed claim(s) is/are <u>1-10, wherein claims 11-38 have</u>	ve been cancelled.		
3. ☑ Acknowledgment is made of a claim for foreign priority ur a) ☑ All b) ☐ Some* c) ☐ None of the:	•		
 Certified copies of the priority documents have Certified copies of the priority documents have 			
Copies of the certified copies of the priority documents have	• • • • • • • • • • • • • • • • • • • •		
International Bureau (PCT Rule 17.2(a)).	cuments have been received in this	national stage application from the	
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements	
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.		
(a) \square including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTO-	948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the C	ffice action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			
6. DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 7/29/2003 4. ☐ Examiner's Comment Regarding Requirement for Deposit	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendn 8. ☑ Examiner's Stateme	(PTO-413), e	
of Biological Material	9.		

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DETAILED ACTION

1. This Office Action responds to the Restriction/Election filed on 7/26/2006, application and IDS filed on 7/29/2003. Claims 1-10 are pending, wherein claims 11-38 have been cancelled by Applicant. Claims 1-10 have been examined and are allowed.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Election/Restrictions

3. Applicant's election without traverse of invention I, claims 1-10 in the reply filed on 7/26/2006 is acknowledged.

Allowable Subject Matter

- 4. Claims 1-10 are allowed.
- 5. The following is an examiner's statement of reasons for allowance:

As per claims 1-10, the independent claims 1,5,9,10, from which the respective claims depend, recites the method/computer program product comprising a computer-readable medium/apparatus for generating an integrated circuit netlist, comprising the inventive steps/means of combining the first and second schematics at corresponding first and second ports within the first and second schematics, respectively, as claimed, which the prior arts made of record failed to teach or suggest. In particular, the prior arts made of record teach various methods/systems of generating schematics, including translating from one type of schematic to another type of schematic and incorporating their parasitic resistances and parasitic capacitances (see especially **Duncan**, U.S.

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Patent No. 5,894,420, especially abstract; col. 7, lines 29-55; **Churchill**, U.S. Patent No. 6,981,238, especially col. 2, line 61 to col. 4, line 10; col. 4, lines 37-57; **Beakes et al.**, U.S. Patent No. 6,131,182, especially col. 10, lines 24-57; **Wanek et al.**, U.S. Patent No. 6,675,118, especially col. 12, lines 12-34; **Dewey et al.**, U.S. Patent Application Publication No. 2004/0003356, especially paragraphs [0007], [0023], [0039], [0049]; **Anderson**, U.S. Patent No. 6,493,648, especially col. 16, lines 43-67; **Landry et al.**, U.S. Patent No. 6,507,932, especially col. 2, lines 19-45; col. 3, line 5 to col. 4, line 52). However, none of the prior arts made of record teach or suggest the inventive steps/means as claimed. In particular, although **Landry et al.** (U.S. Patent No. 6,507,932) teach the conversion of one type of schematic to another type of schematic, the resulting schematic is not the result of the combination of two schematics combined as claimed. Accordingly, the claimed invention is novel and un-obvious over the prior arts made of record.

Conclusion

- 6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phallaka Kik whose telephone number is 571-272-1895. The examiner can normally be reached on Monday-Thursday, 8:30AM-7PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on 571-272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Any response to this action should be mailed to:

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

or faxed to:

Phallaka Kik

Primary Examiner September 16, 2006